

***IN THE UNITED STATES PATENT AND TRADEMARK OFFICE***

Applicant: Toru TATSUMI et al.

Title: SEMICONDUCTOR DEVICE  
HAVING A MIS-TYPE FET,  
AND METHODS FOR  
MANUFACTURING THE  
SAME AND FORMING A  
METAL OXIDE FILM

Appl. No.: 10/558,271

International Filing Date: 5/31/2004  
371(c) Date: 10/16/06

Examiner: Jami M. Valentine

Art Unit: 2815

Confirmation Number: 1477

**AMENDMENT AND REPLY UNDER 37 CFR 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated March 7, 2008, concerning the above-referenced patent application.

**Amendments to the Specification** begin on page 3 of this document.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 4 of this document.

**Amendments to the Drawings** begin on page 6 of this document, and include attached replacement drawing sheets.

**Remarks/Arguments** begin on page 7 of this document.

Please amend the application as follows: